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PATENT
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IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Shigemasa et al. Conf.: 3009
Appl. No.: 09/552,701 Group: 2184
Filed: April 19, 2000 Examiner: PUENTE, EMERSON
For: MICROCOMPUTER HAVING BUILT-IN
NONVOLATILE MEMORY AND CHECK SYSTEM
THEREOF AND IC CARD PACKING
MICROCOMPUTER HAVING BUILT-IN
NONVOLATILE MEMORY AND CHECK SYSTEM
THEREOF

REPLY

Assistant Commissioner for Patents
Washington, DC 20231

March 18, 2003

Sir:

In reply to the Office Action dated December 18, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS

Please cancel claim 2 without prejudice or disclaimer of the subject matter contained therein.